

Arquitetura de Sistemas Operacionais

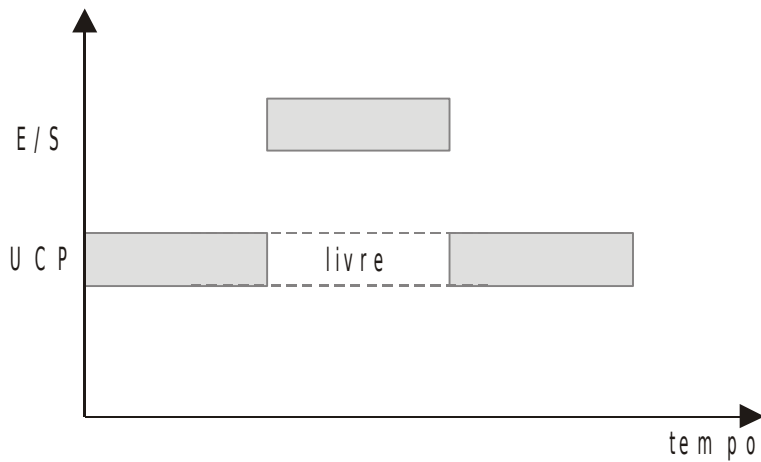
Francis Berenger Machado / Luiz Paulo Maia

Capítulo 3

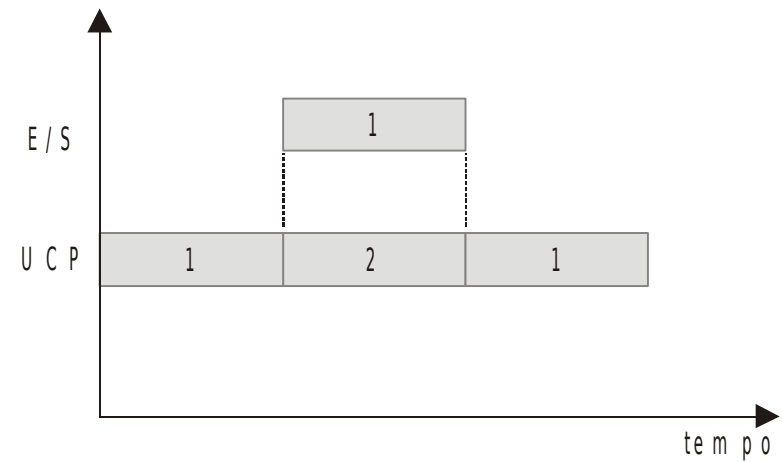
Concorrência

3 – Concorrência

▪ Sist. Monoprogramável x Sist. Multiprogramável

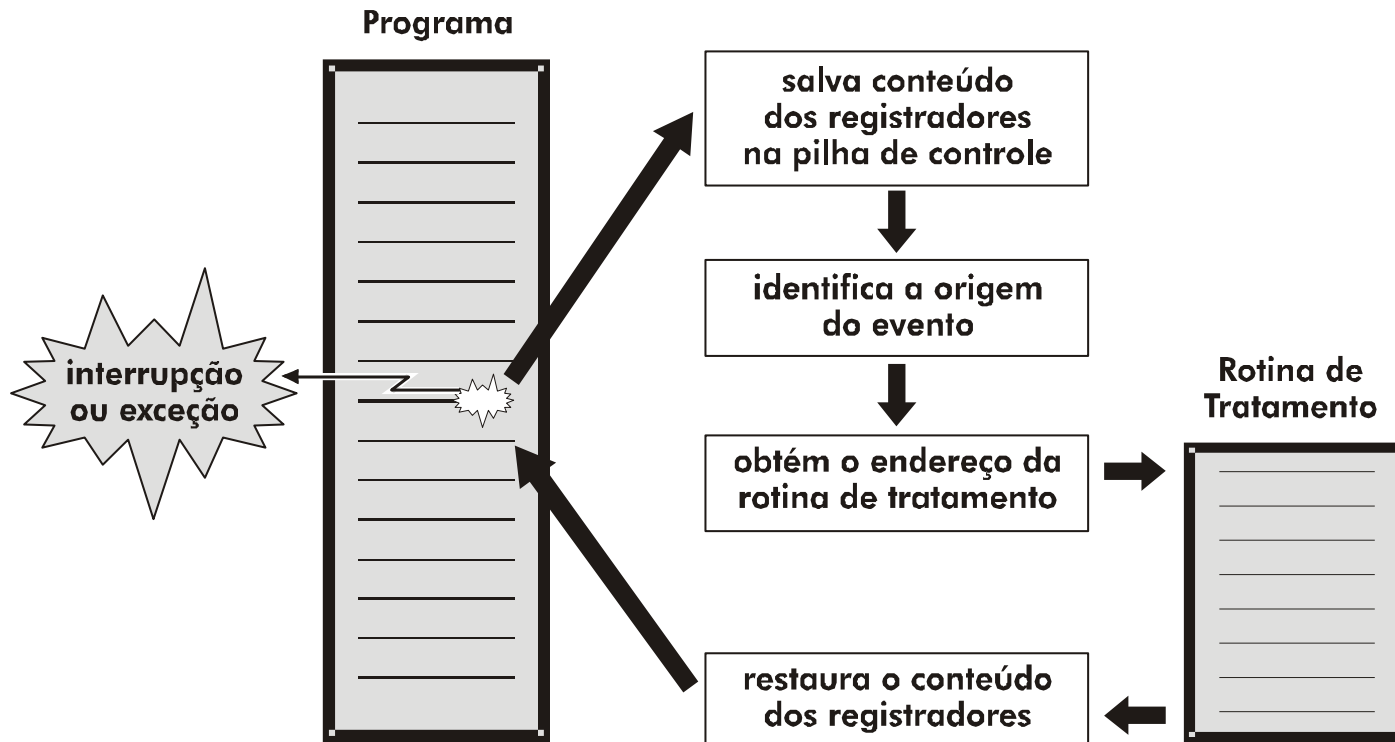


(a) Sistema Monoprogramável

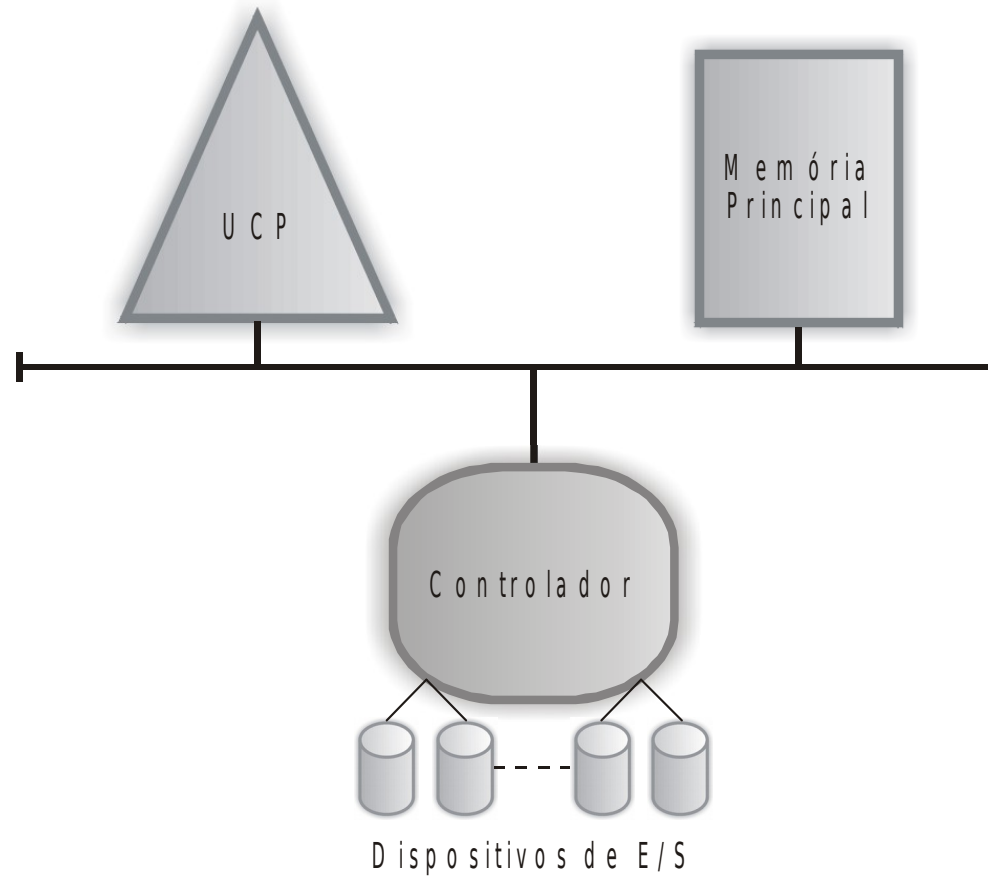


(b) Sistema Multiprogramável

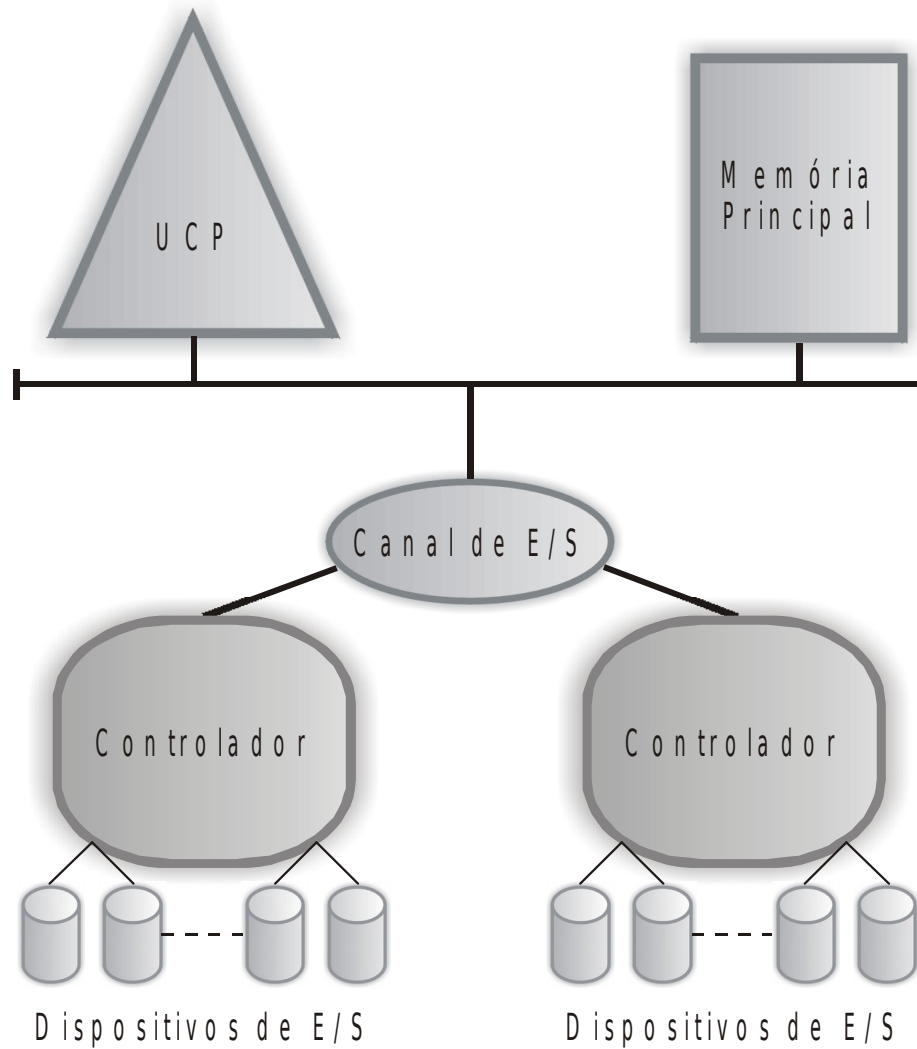
▪ Interrupção e Exceção



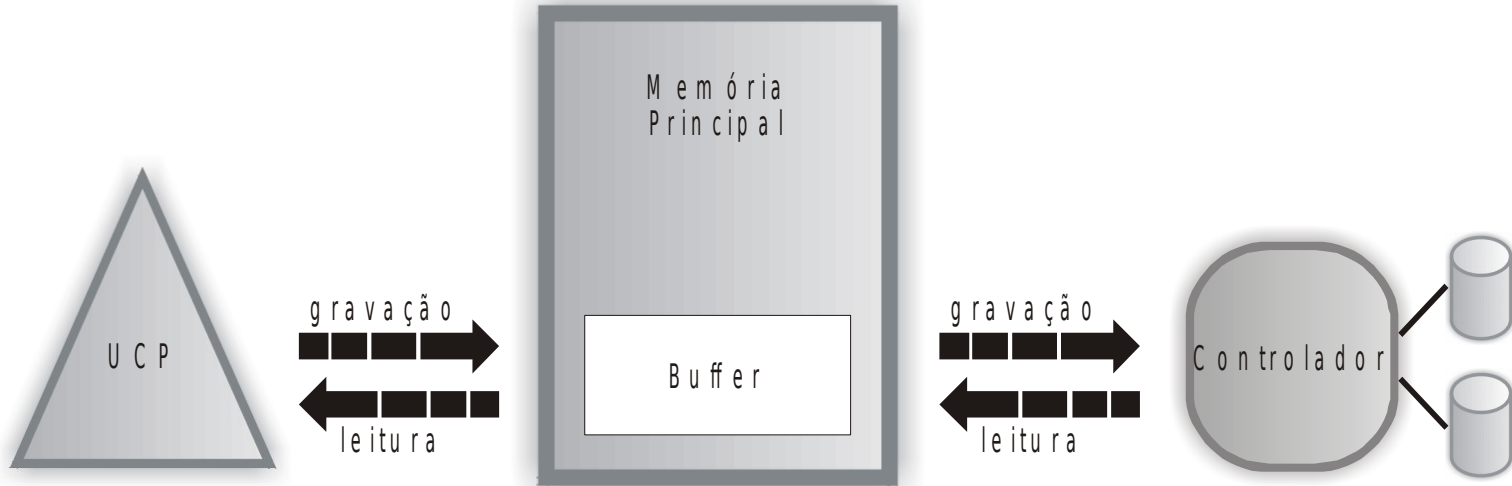
▪ Controlador



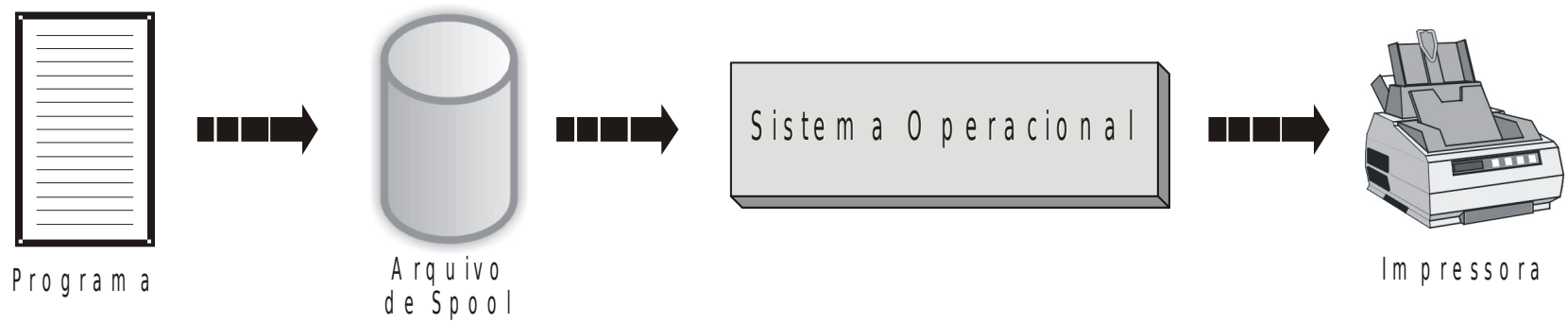
▪ Canal de E/S



▪ Buffering



▪ Spooling



▪ Reentrância

